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In re Patent Application	OIPE	Atty Dkt.	1035-471				110
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KIMURA et al	B JAN 0 3 2005 HT		2811				
Serial No. 10/670,194	VA O	Examiner:	Vu, Q.				
Filed: September 26, 2	ADEM		January 3, 2005				
Title: CHIP-STACK SEN THE SAME		E AND MANUFA	CTURING METHO	DD OF			
² Commissioner for Paten P.O. Box 1450 Alexandria, VA 22313-1							
Sir:		~~~~					
This is a response/amen incorporated by referenc signature thereon.	dment/letter in the ab	ove-identified a	NDMENT/LETTE pplication and in the signature to	cludes an attachment w	hich is h sence c	iereby of any o	other
-	ce Address Indic	ation Form A	Attached.				
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Independent claims after previously paid for 3			hest number 200.00	\$0.00 (1201)/\$0.00	(2201)	\$	
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Terminal disclaimer end	losed. add			\$130.00 (1814)/ \$65.00		\$	
Applicant claims "sr		Statement fi	led herewith		, ,		
Rule 56 Information Dis				\$180.00 (⁻	1806)	\$	
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0.101.				TOTAL FEE ENCL			0.00
The Commissioner is he asserted to be filed, or w firm) to our Account No.	hich should have bee	en filed herewith	(or with any pap	y overpayment, in the fe	e(s) file	d, or	
1100 North Glebe Road, Arlington, Virginia 22201	-4714		I & VANDERHYE	E P.C. a, Reg No. 37,515			
Telephone: (703) 816-4 Facsimile: (703) 816-41		.					
JAR:caj		Signati					



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

KIMURA et al

Atty. Ref.: 1035-471; Confirmation No. 4479

Appl. No. 10/670,194 TC/A.U. 2811

Filed: September 26, 2003 Examiner: Vu, Q.

For: CHIP-STACK SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD OF THE SAME

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January 3, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

AMENDMENT

Responsive to the Official Action dated October 6, 2004, please amend the

above-identified application as follows: